AMENDMENTS TO THE CLAIMS

This listing of claims replaces all prior versions and listings of claims in the application.

1. **(Currently Amended)** A receiver circuit, comprising:

an optical receiving device configured to convert a received optical data signal into an electrical signal, and further configured to output the electrical signal;

a plurality of amplifiers which are connected to the optical receiving device, wherein the plurality of amplifiers each include a supply voltage connection and at least a separate input amplifier stage and a separate output amplifier stage, wherein the input amplifier stages of the plurality of amplifiers are each coupled to the output of the optical receiving ~~element~~ device, wherein the individual amplifiers of the plurality of amplifiers are connected in parallel with one another, and wherein the input amplifier stages of the plurality of amplifiers are only coupled to the output amplifier stage of the same individual amplifier;

a plurality of electrical switches, each of which is arranged in series between the optical receiving device and a respective amplifier;

circuit means for individually activating and deactivating the individual amplifiers by regulating a supply voltage to each of the amplifiers or by controlling the electrical switches;

a detecting circuit for detecting [[the]] a bandwidth of [[a]] the electrical signal ~~which has been detected~~ produced by the optical receiving device; and

one or more control lines connecting the detecting circuit with the circuit means for individually activating and deactivating the individual amplifiers;

wherein the detecting circuit is configured to provide control signals to the circuit means via the one or more control lines for activating the one of the plurality of amplifiers most suited to amplify the bandwidth ~~detected~~ of the electrical signal received by the detecting circuit;

wherein the amplifiers each differ from one another in at least one parameter, and

wherein the plurality of electrical switches enable the electrical signal from the optical receiving device to supplied to only one amplifier ~~is activated~~ at a given point in

time and enable supply of the electrical signal from the optical receiving device to the other amplifiers ~~are deactivated~~ to be prevented at that given point in time.

2. **(Canceled)**

3. **(Canceled)**

4. **(Currently Amended)** The receiver circuit according to Claim 1, wherein the amplifiers each have an input connected to the receiving device by way of one of the plurality of switches, and the amplifiers each have an output, and wherein the circuit means is operable to activate or deactivate each of the individual amplifiers by switching the output on or off ~~for the purpose of individually activating and deactivating the individual amplifiers~~.

5. **(Canceled)**

6. **(Canceled)**

7. **(Previously Presented)** The receiver circuit according to Claim 1, wherein the amplifiers each comprise a transimpedance amplifier.

8. **(Previously Presented)** The receiver circuit according to Claim 1, wherein the input amplifier stage and the output amplifier stage of the individual amplifiers of the plurality of amplifiers are connected in series.

9. **(Previously Presented)** The receiver circuit according to Claim 8, wherein at least the input amplifier stage, that is connected to the receiving device comprises a transimpedance amplifier.

10. **(Cancelled)**

11. **(Previously Presented)** The receiver circuit according to Claim 1, wherein the one parameter in which the individual amplifiers differ is the gain.

12. **(Previously Presented)** The receiver circuit according to Claim 1, wherein the circuit means comprise a plurality of switches that are set individually.

13. **(Previously Presented)** The receiver circuit according to Claim 12, wherein the individual switches comprise MOS transistors.

14. **(Previously Presented)** The receiver circuit according to Claim 1, wherein the circuit means is adjusted via at least one control line.

15. **(Previously Presented)** The receiver circuit according to Claim 1, wherein the receiving device comprises a photodiode.

16. **(Previously Presented)** The receiver circuit according to Claim 1, wherein the individual amplifiers are monolithically integrated in a common chip.

17. – 21. **(Canceled)**